Attorney Docket No.: CYPR-CD00184

# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
## Patent Application

Inventor(s): ·Craig Nemecek

Application No.: 09/975,105          Group Art Unit:

Filed: 10/10/01          Examiner:

Title: HOST TO FPGA INTERFACE IN AN IN-CIRCUIT EMULATION SYSTEM

## Form 1449

### U.S. Patent Documents

| Examiner Initial | No. | Patent No. | Date | Patentee | Class | Sub-class | Filing Date |
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### Foreign Patent or Published Foreign Patent Application

| Examiner Initial | No. | Document No. | Publication Date | Country or Patent Office | Class | Sub-class | Translation Yes | No |
|---|---|---|---|---|---|---|---|---|
| | B | | | | | | | |

### Other Documents

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| Examiner | Date Considered |
|---|---|
| Thau Phan | 1/17/06 |

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.